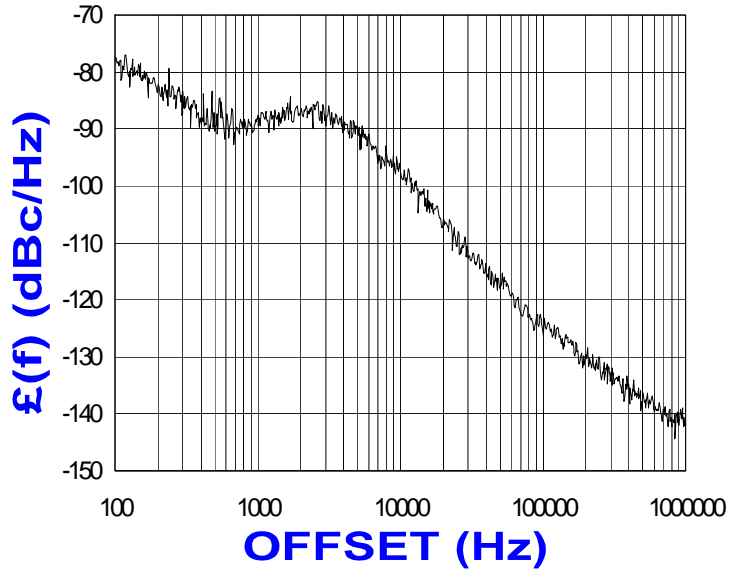


PHASE NOISE (1 Hz BW, typical)



FEATURES	
• Frequency Range:	1600 - 1700 MHz
• Step Size:	1000 KHz
• PLL - Style Package	
APPLICATIONS	
• Telecommunications	
• Satellite	
• Telemetry	

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	1600 - 1700	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-98	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	1.25±2.25	dBm
Load Impedance	50	Ω
Step Size	1000	KHz
Charge Pump Output Current	1250	μ A
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	2	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	PLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	35	mA

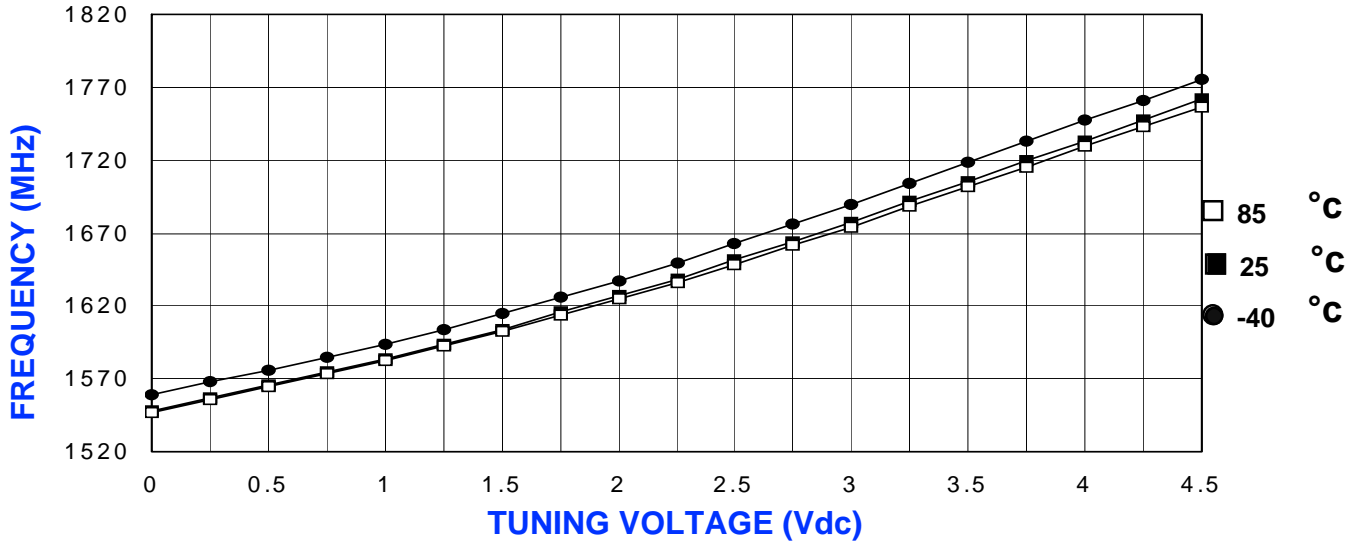
All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES	
• AN-107 : How to Solder Z-COMM VCOs / PLLs	
• AN-200 : Mounting and Grounding of Z-COMM PLLs	
• AN-201 : PLL Fundamentals	AN-202 : PLL Functional Description

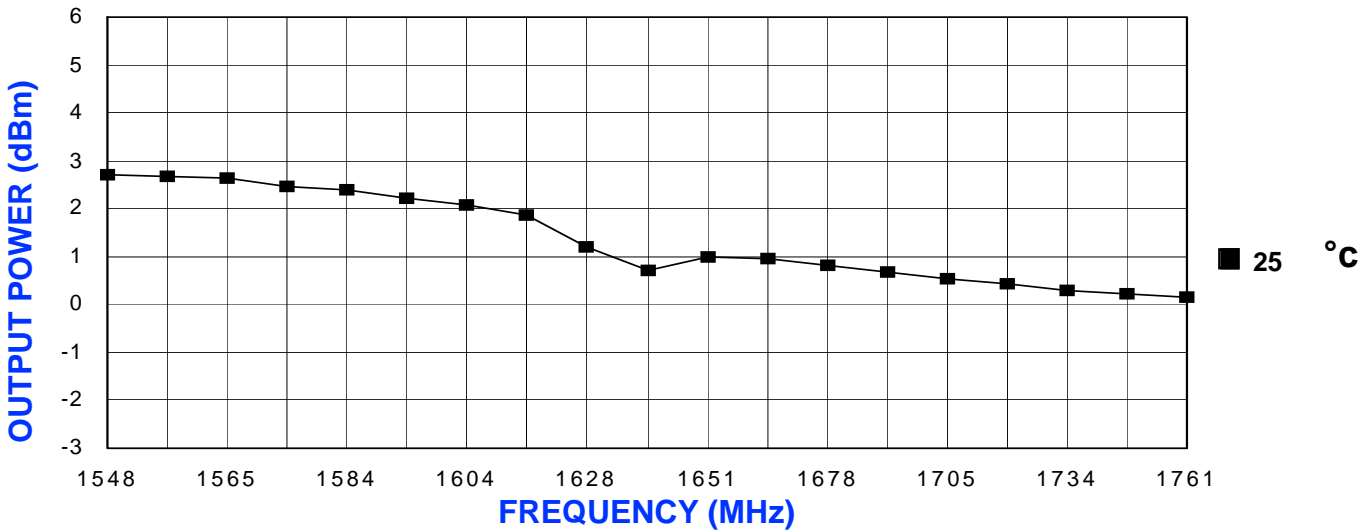
NOTES:

Reference Oscillator Signal: $5 \text{ MHz} < f_{osc} < 100 \text{ MHz}$
 Frequency Synthesizer: National Semiconductor - ADF4106

VCO TUNING CURVE, typ.



VCO POWER CURVE, typ.



PHYSICAL DIMENSIONS

